

Docket No.: M4065.0103/P103-A  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Howard E. Rhodes

Application No.: Not Yet Assigned

Confirmation Number:

Filed: **18 AUG 2003**

Art Unit: N/A

For: BURIED CHANNEL CMOS IMAGER AND  
METHOD OF FORMING SAME

Examiner: Not Yet Assigned

**SUBMISSION OF FORMAL DRAWINGS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (9 sheets, figures 1-13) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed formal drawings for the drawings submitted with the originally filed application. Applicant notes that Figure 5A was approved and added during prosecution of the parent application.

Dated: **18 AUG 2003**

Respectfully submitted,

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